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FIG. 2 (Amended)

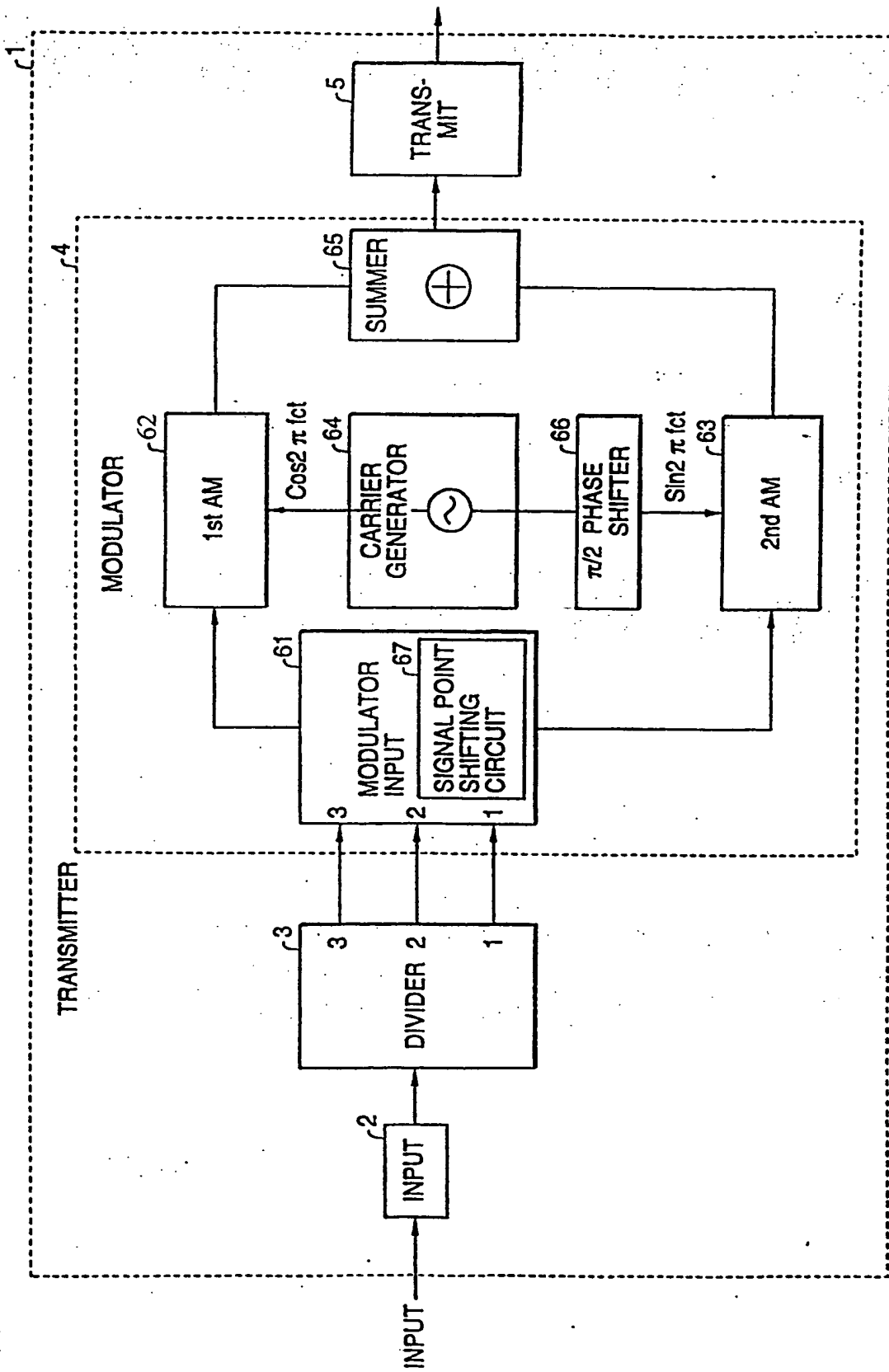


FIG. 10 (Amended)

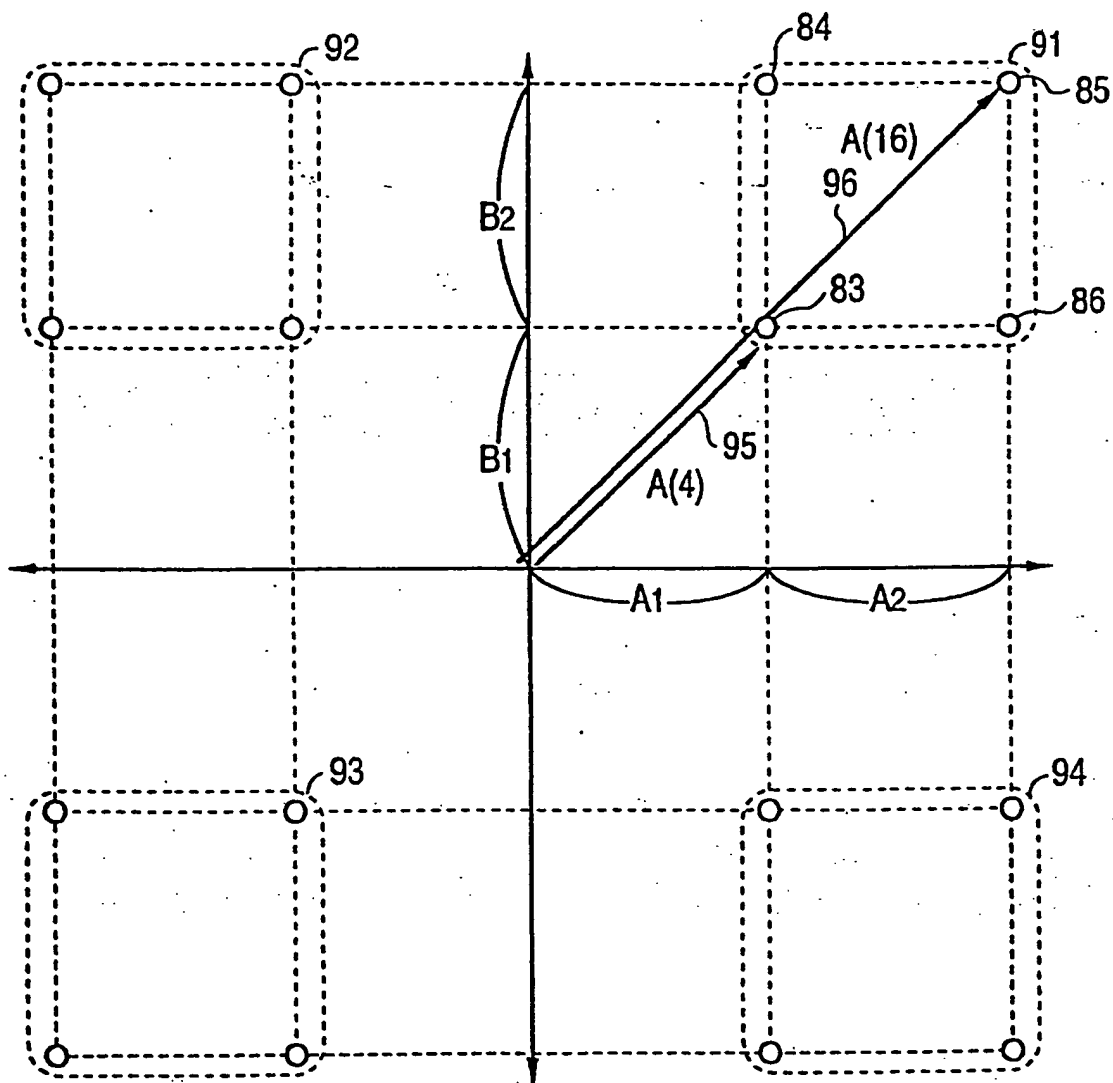


FIG. 17 (Amended)

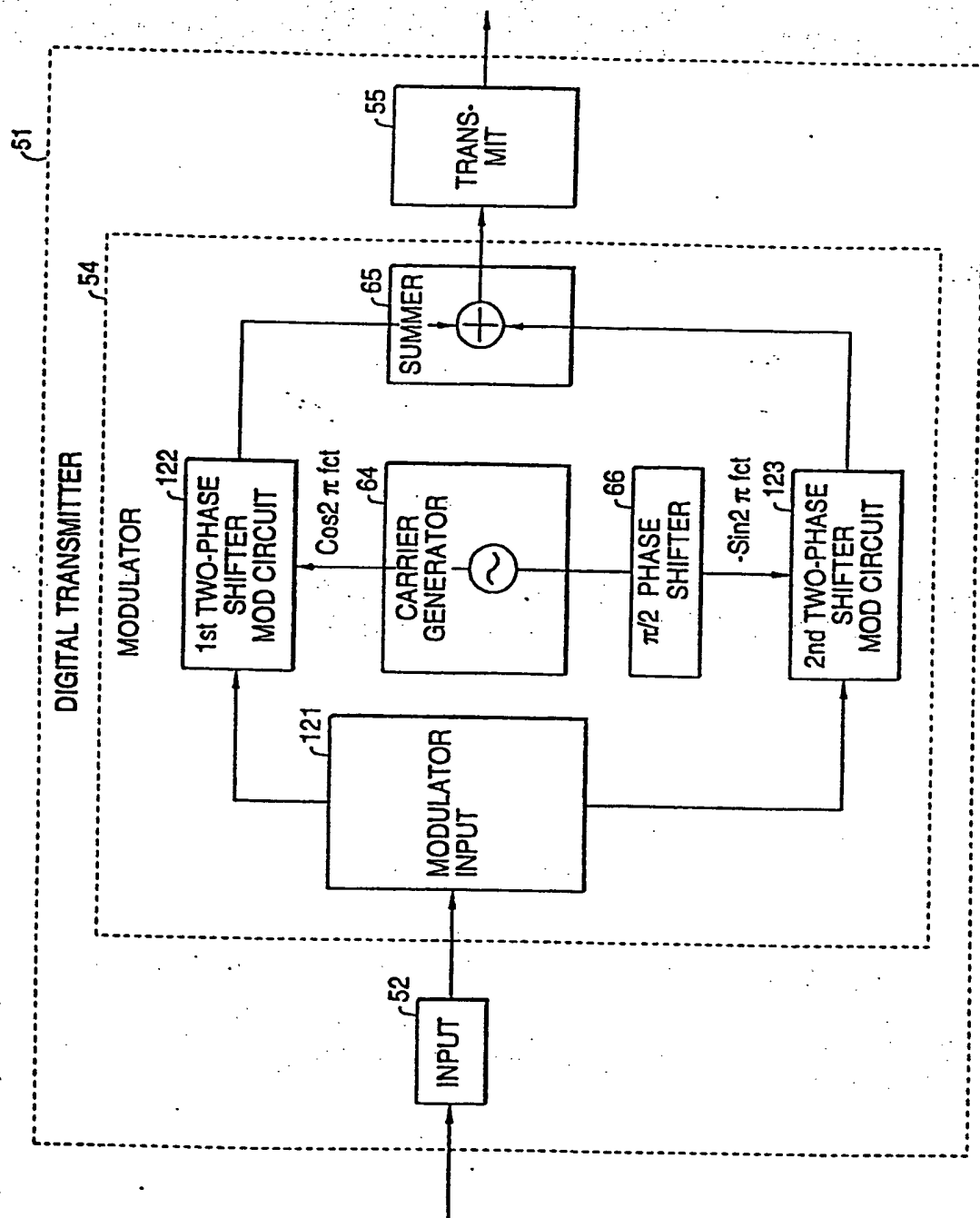


FIG. 29 (Amended)

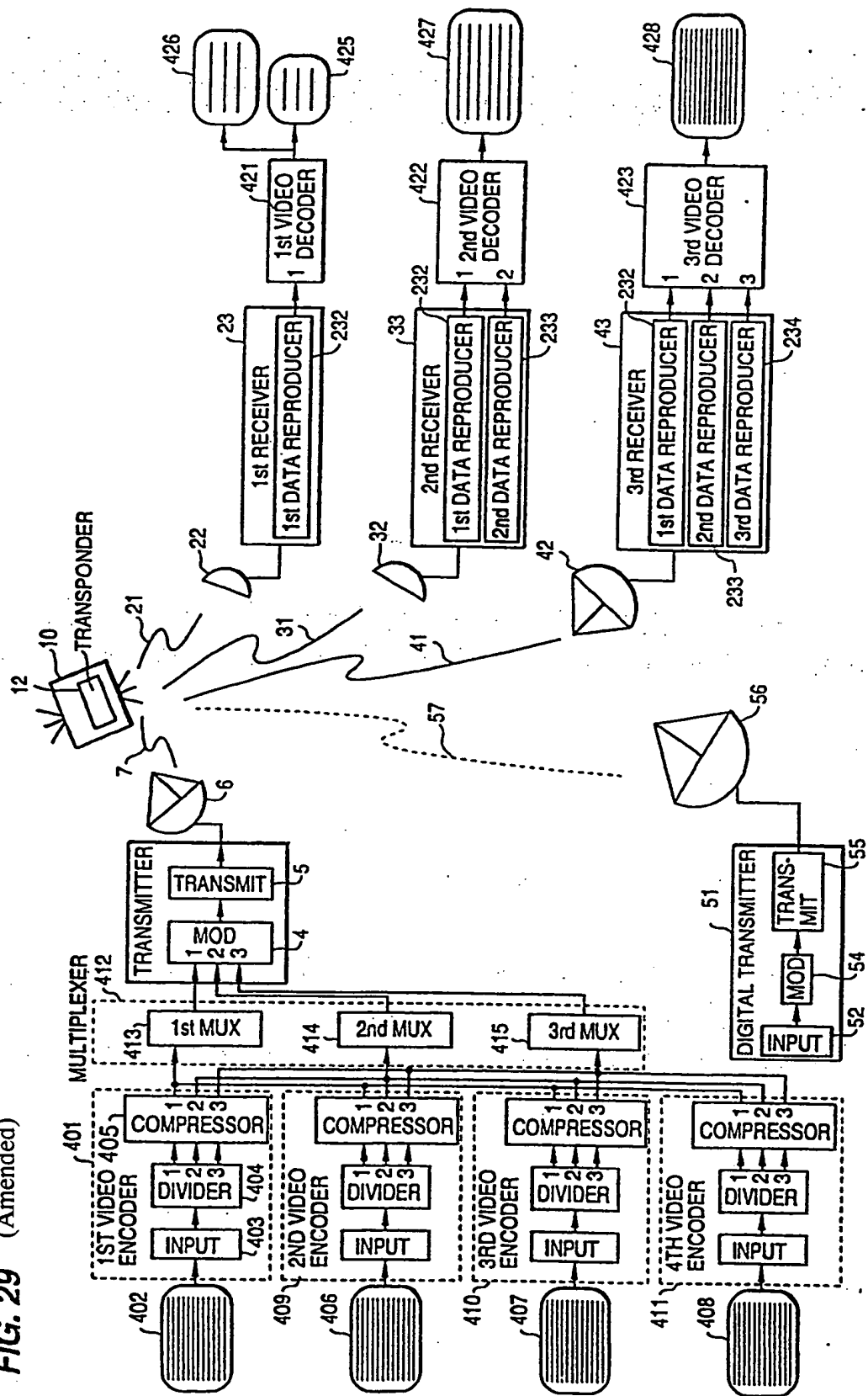
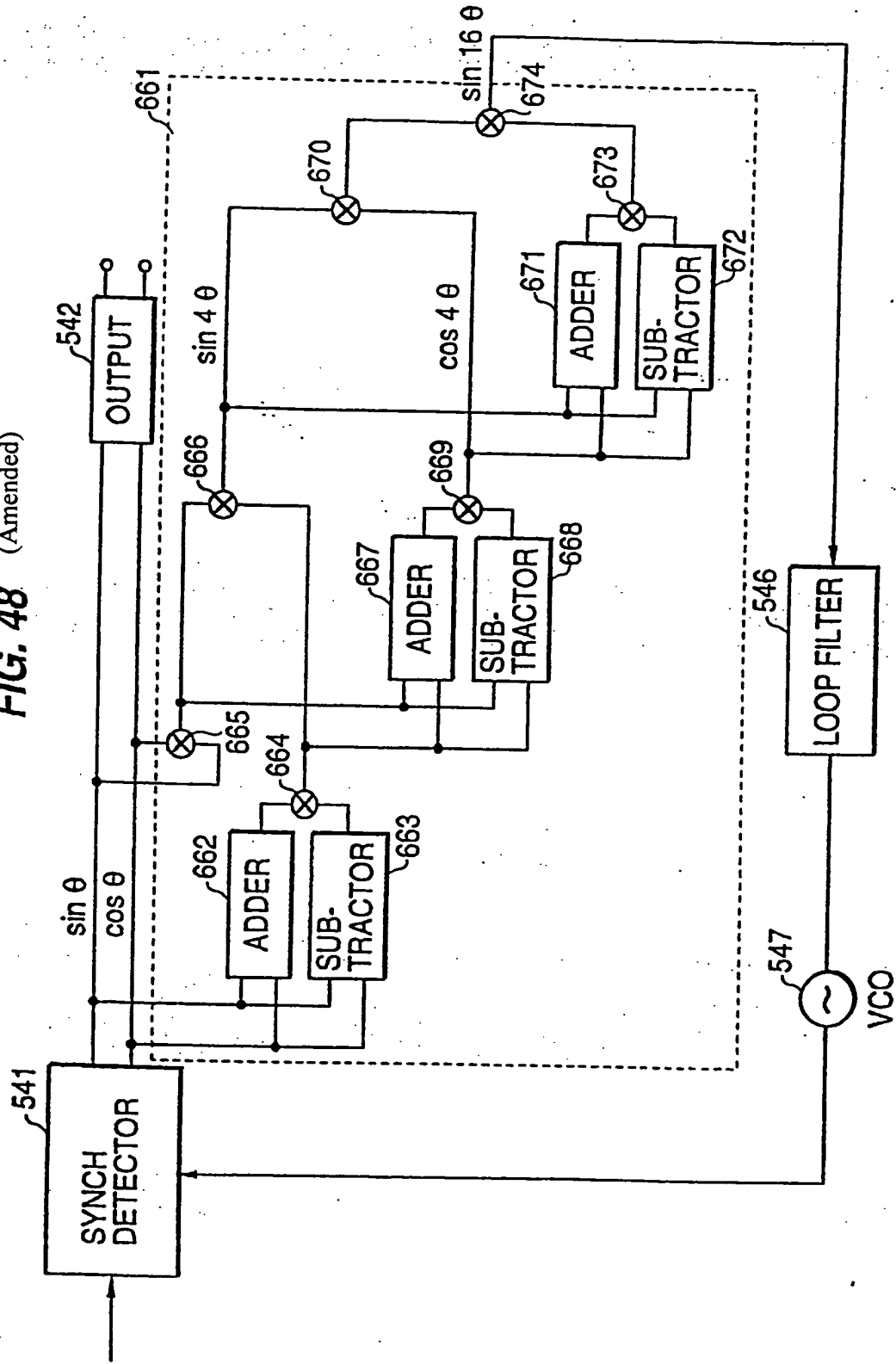
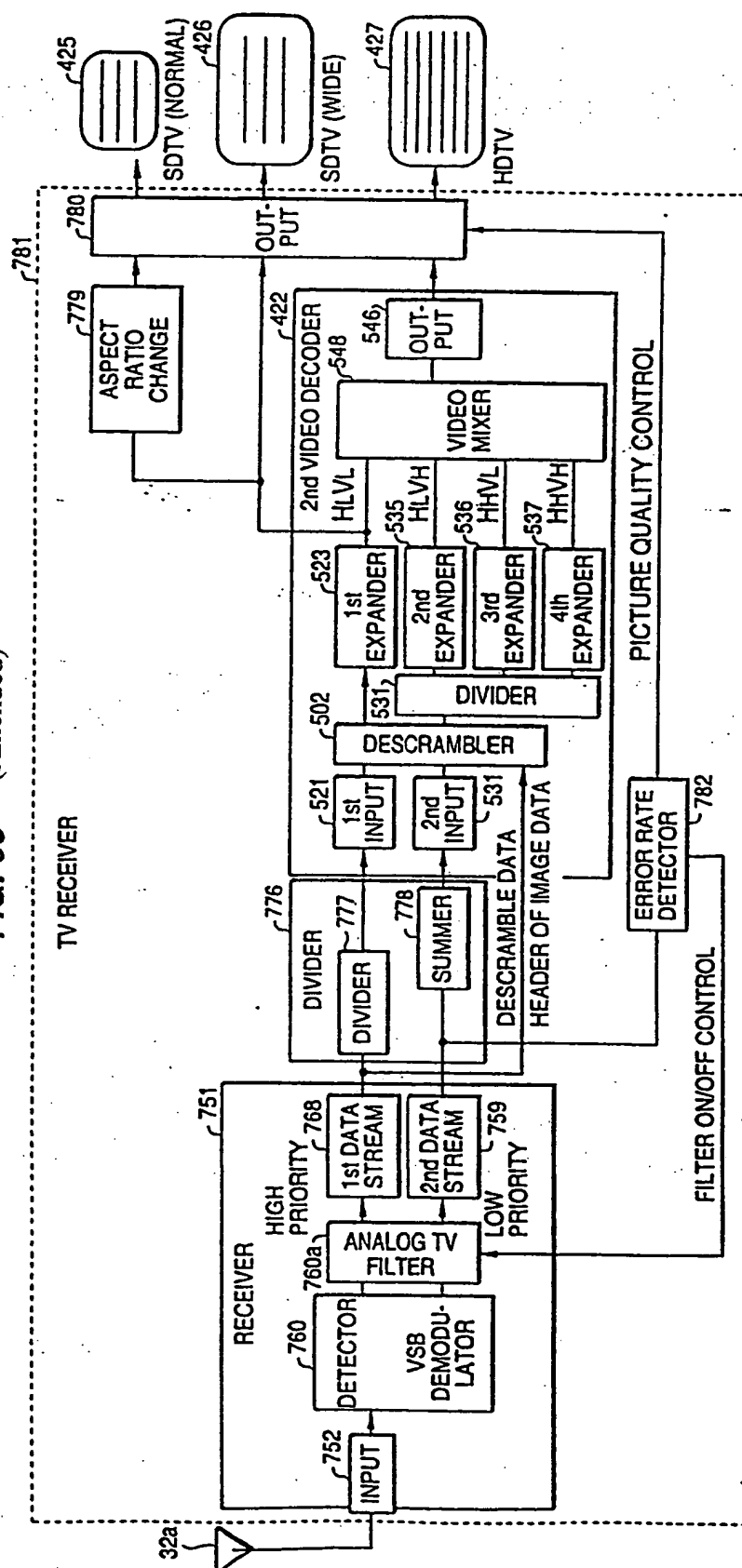


FIG. 48 (Amended)



32a:



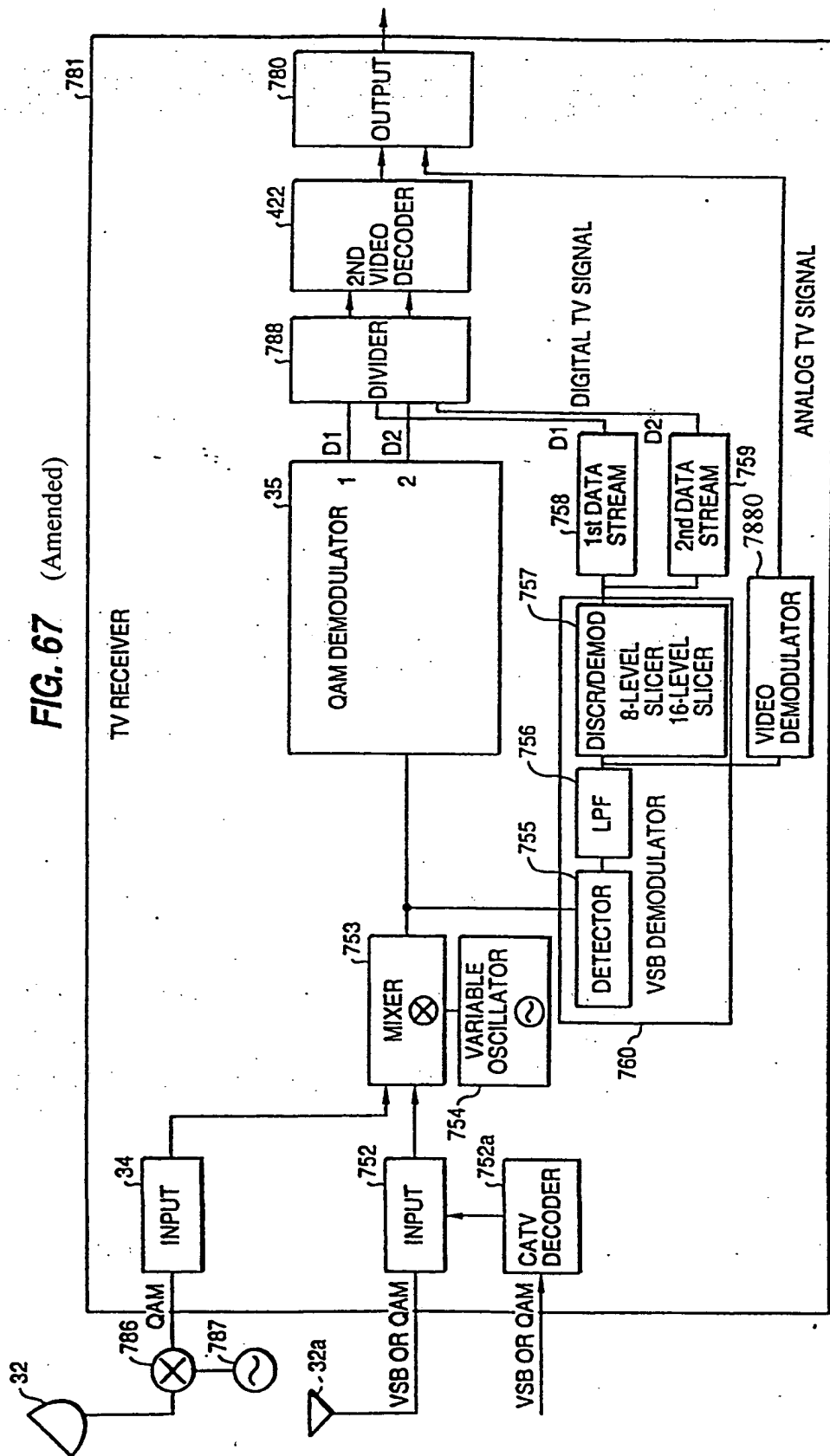


FIG. 67 (Amended)

FIG. 93 (Amended)

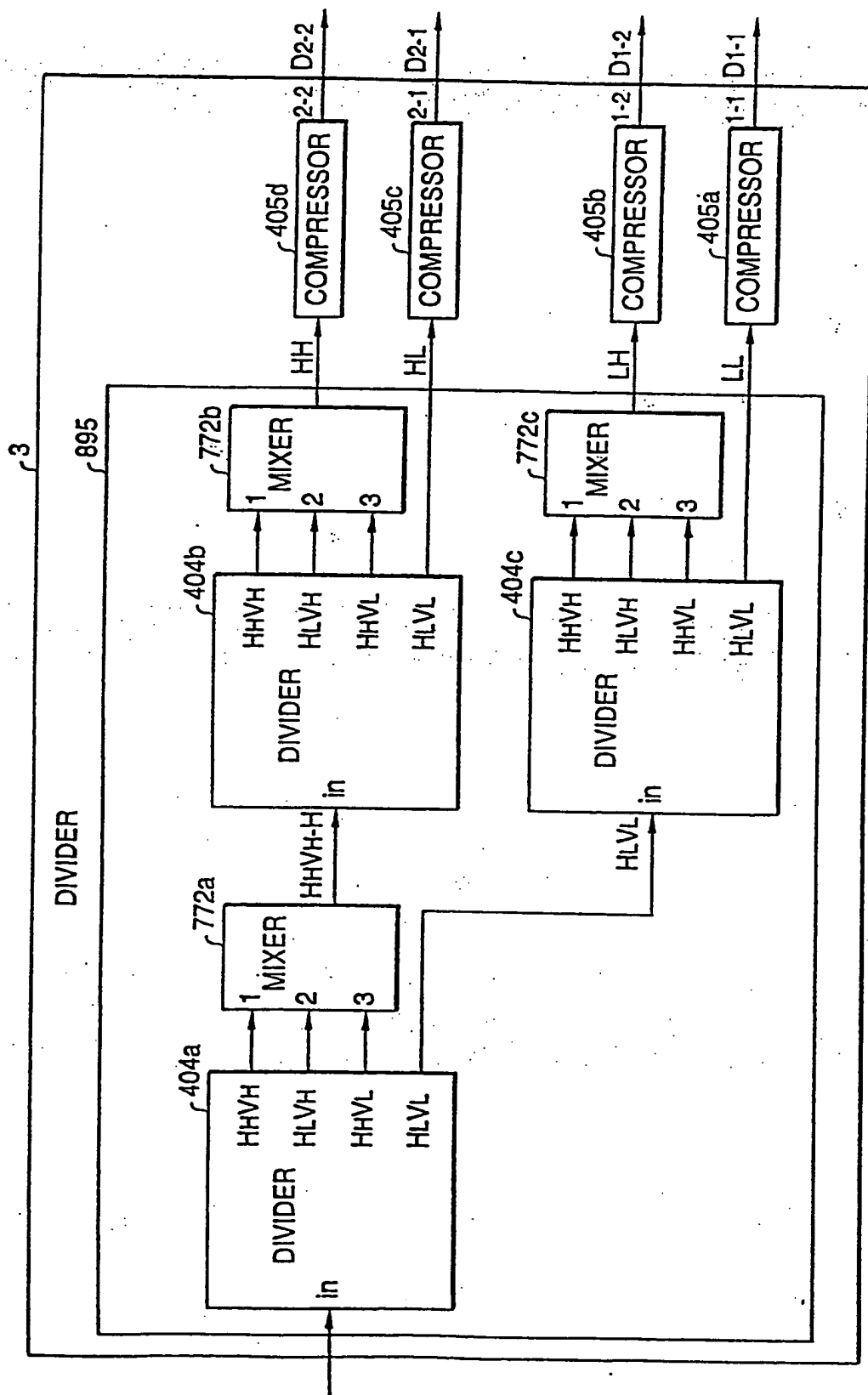
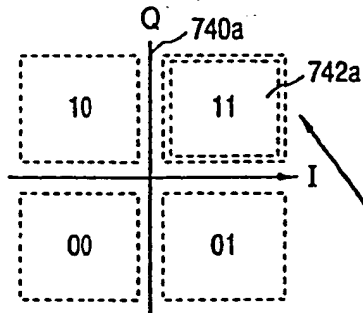
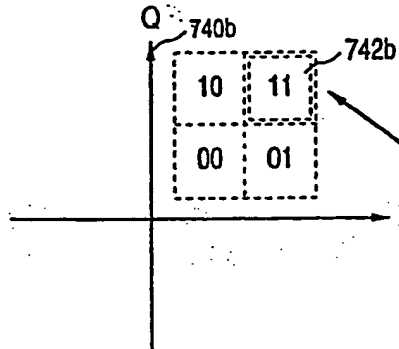


FIG. 112 (Amended)

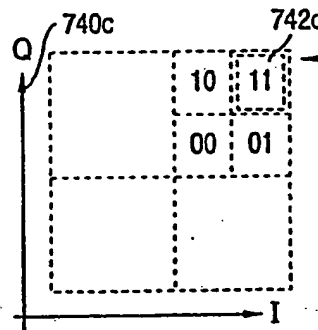
SUBCHANNEL-1 (SRQAM:D1 = 2bit)



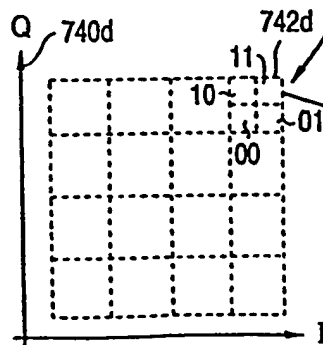
SUBCHANNEL-2 (16-SRQAM:D2 = 2bit)



SUBCHANNEL-3 (64-SRQAM:D3 = 2bit)



SUBCHANNEL-4 (256-SRQAM:D4 = 2bit)

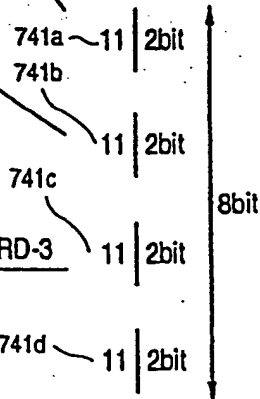


CODE WORD-1

CODE WORD-2

CODE WORD-3

CODE WORD-4



SIGNAL POINT
CODE WORD
11 11 11 11

FIG. 131 (Amended)

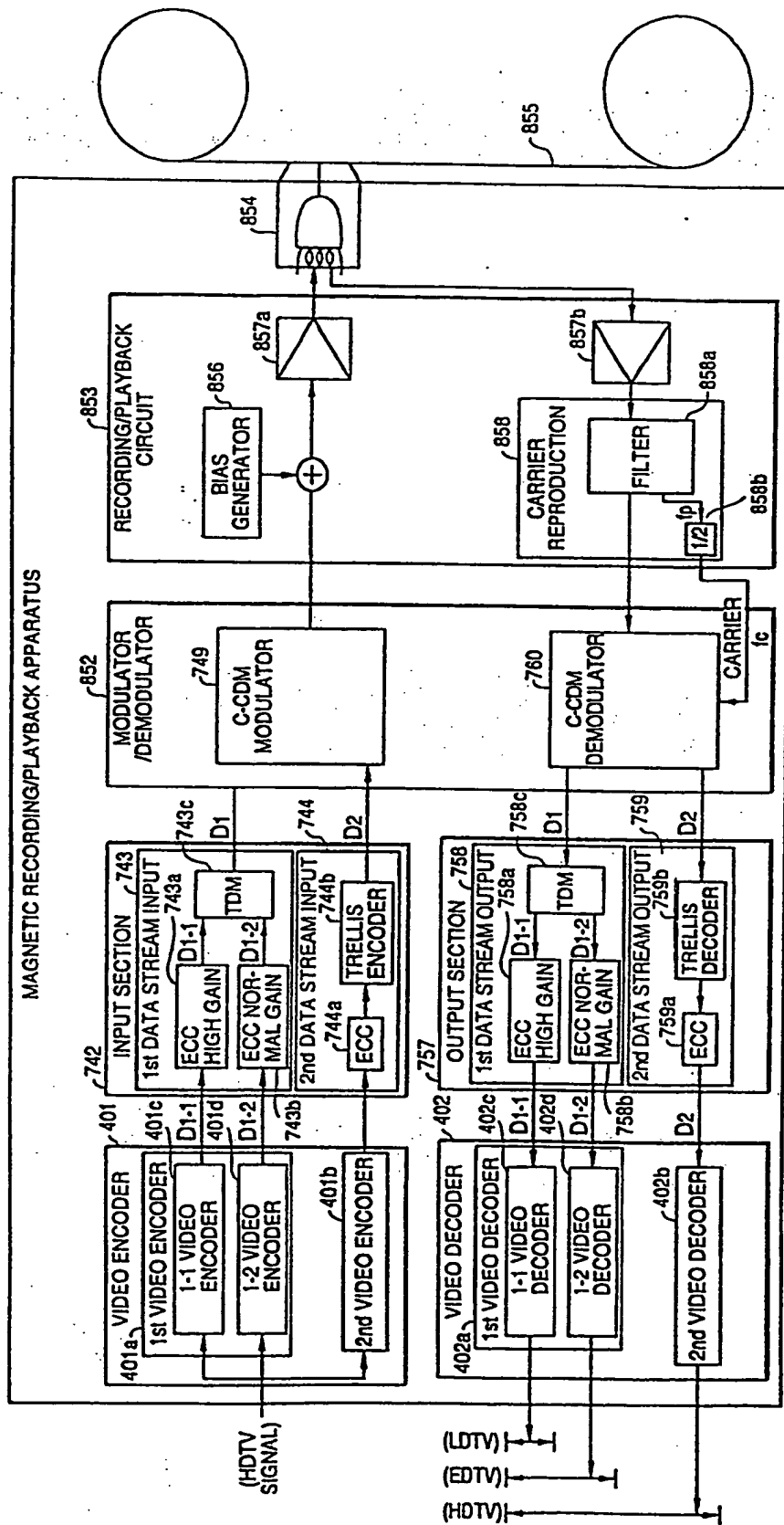


FIG. 138 (Amended)

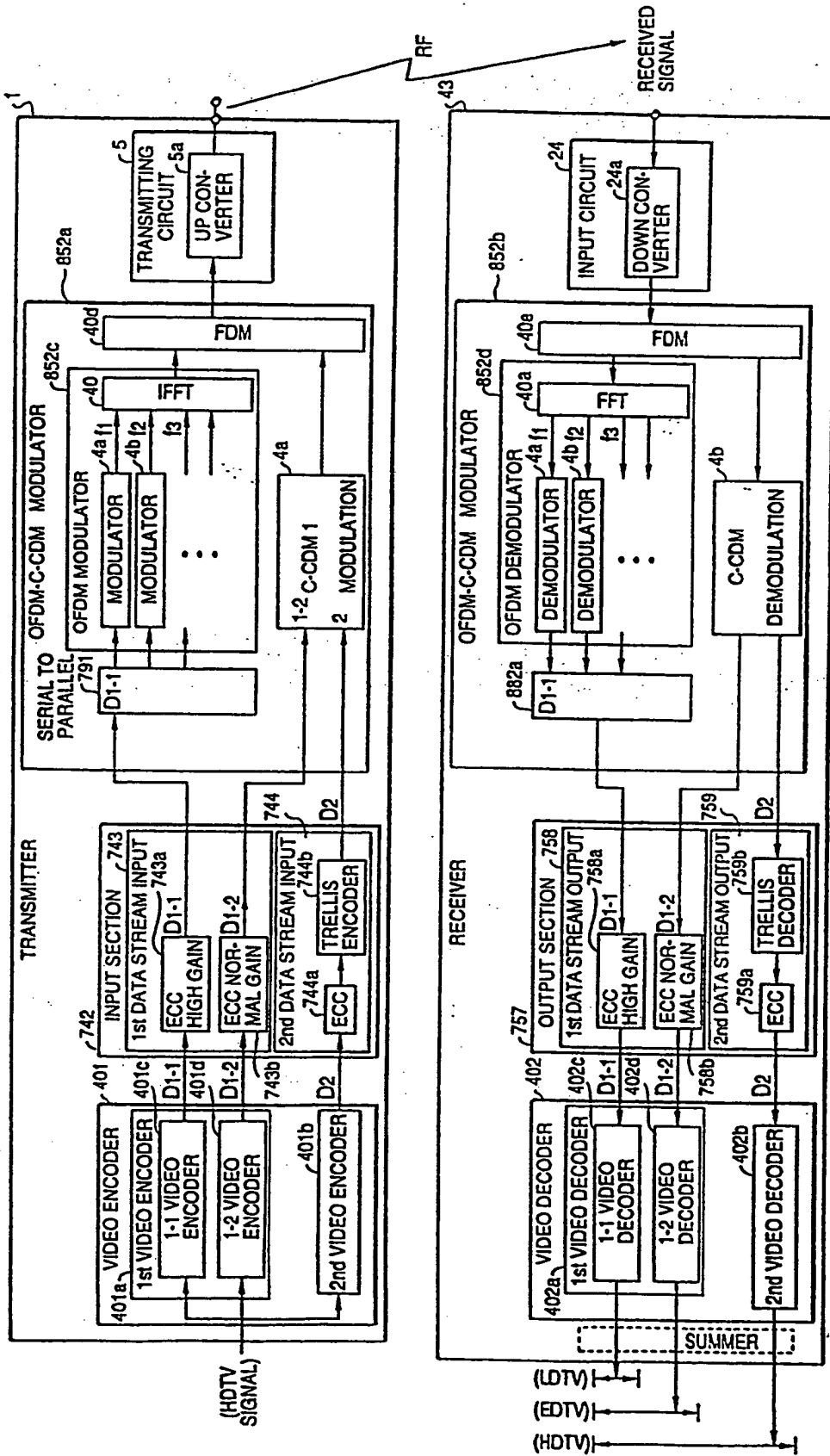


FIG. 144 (Amended)

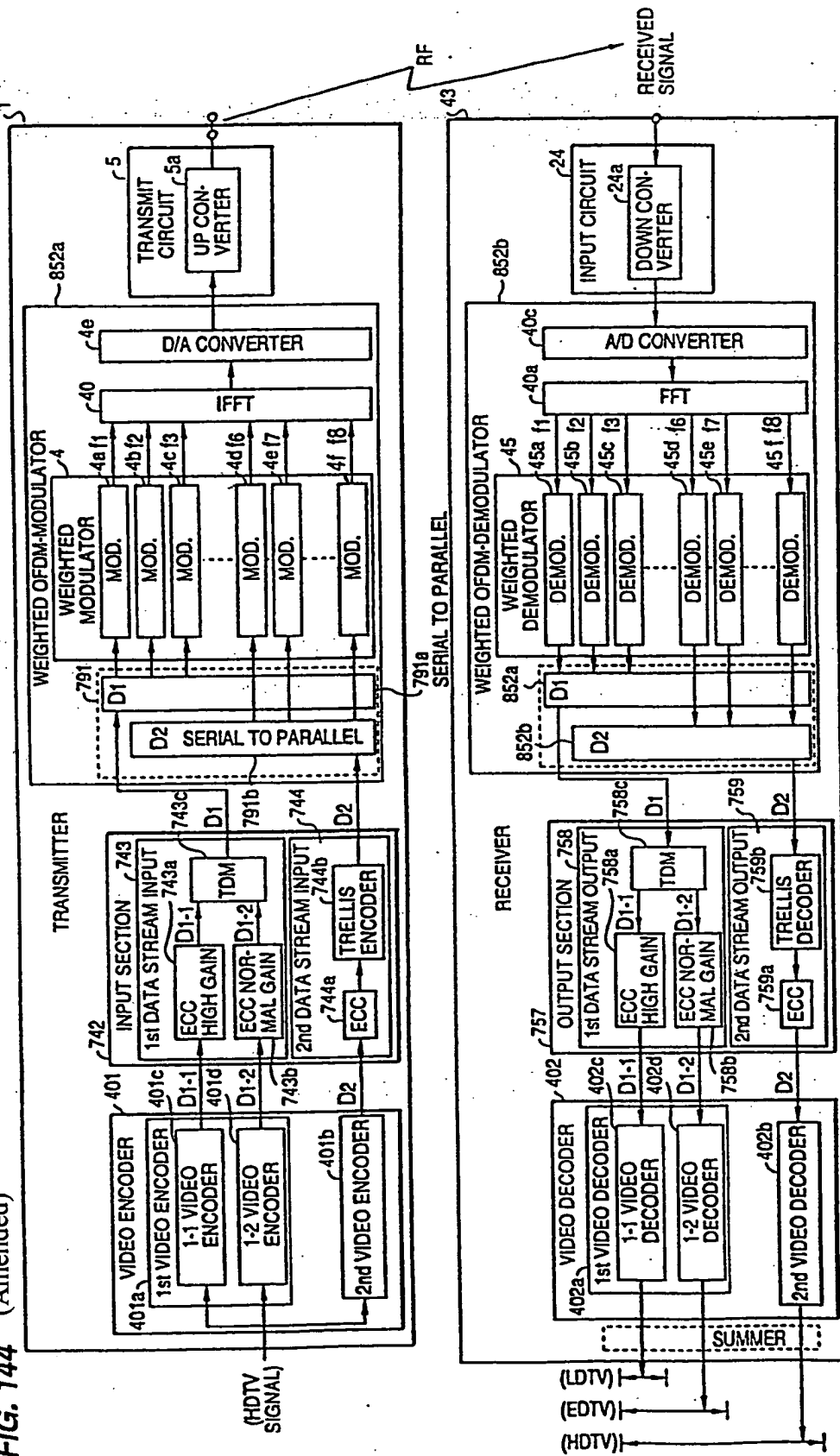


FIG. 169 (Amended)

